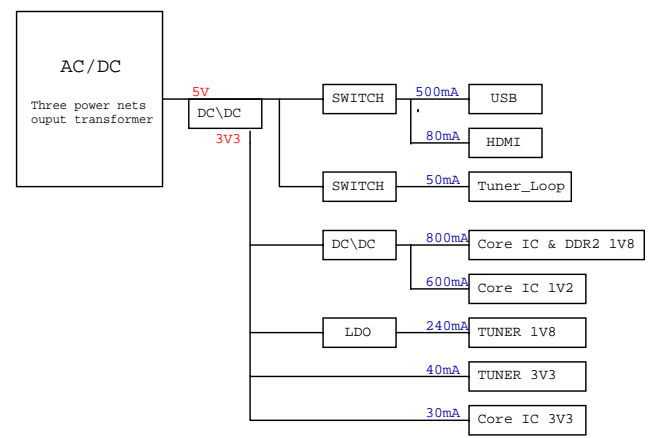
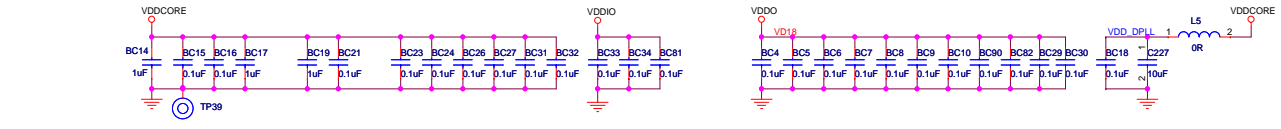
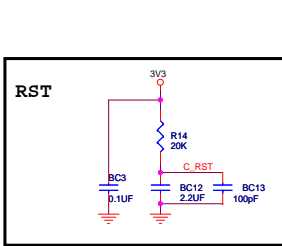
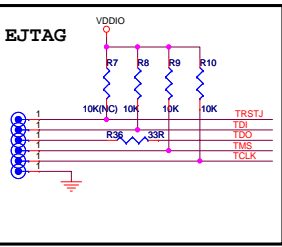
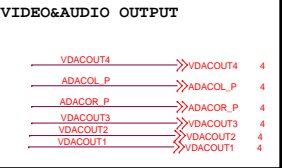
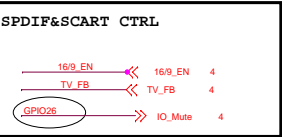
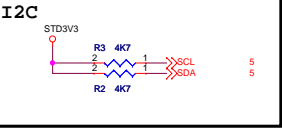
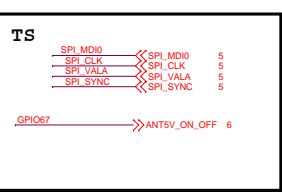
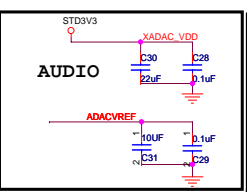
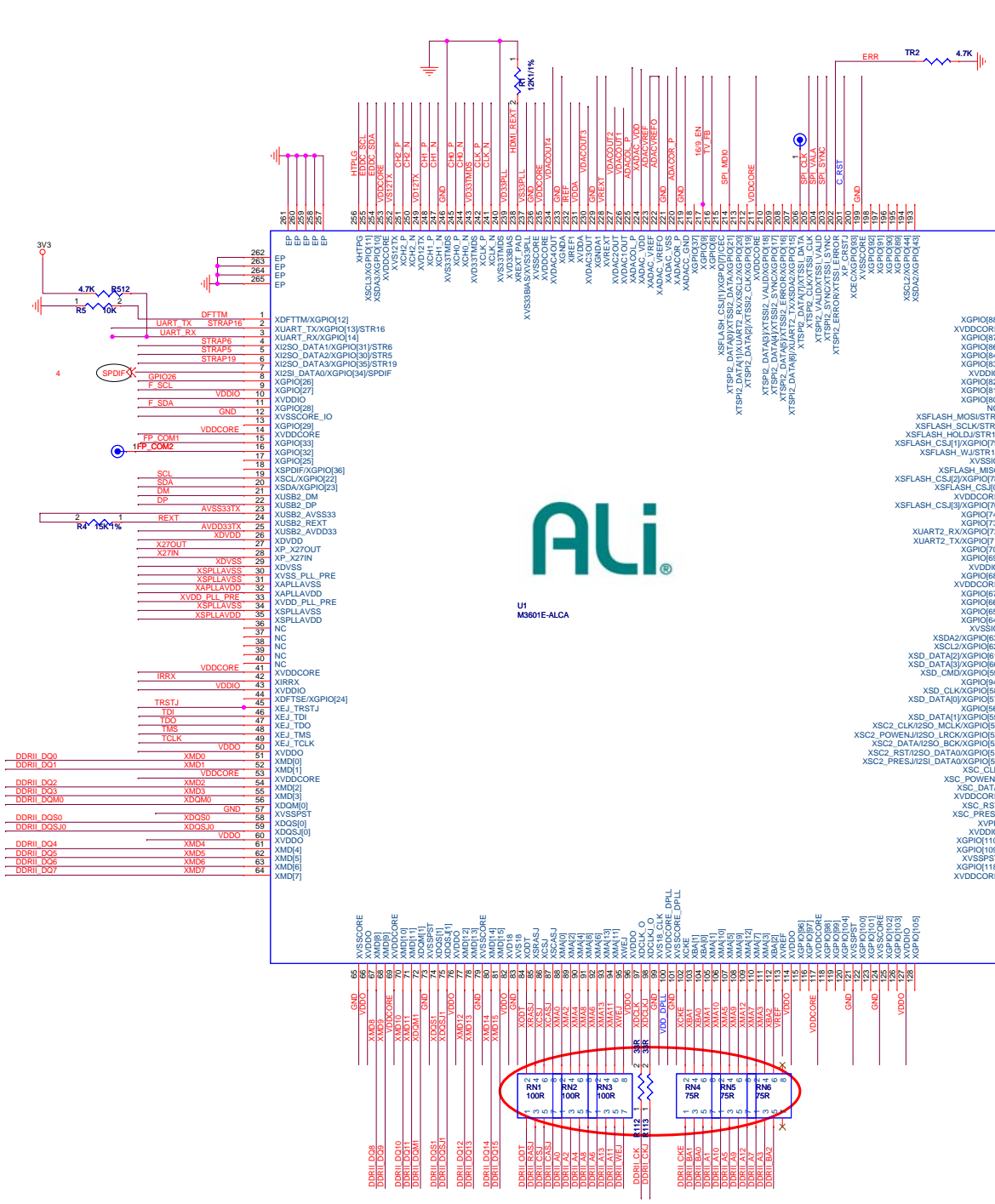
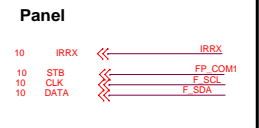
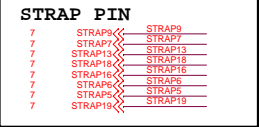
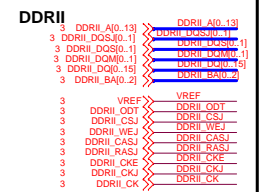
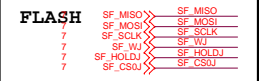
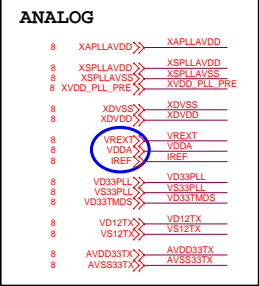
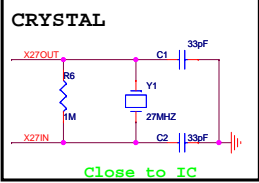
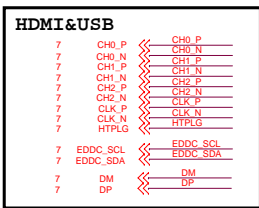
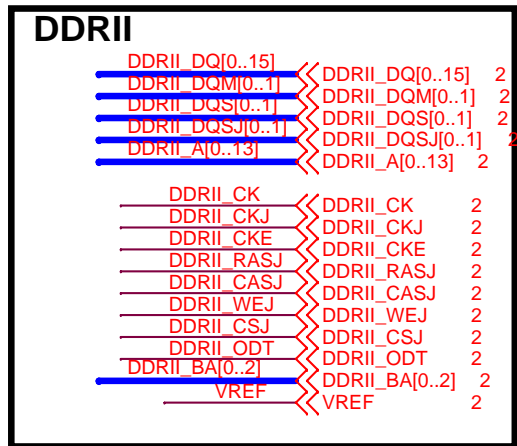
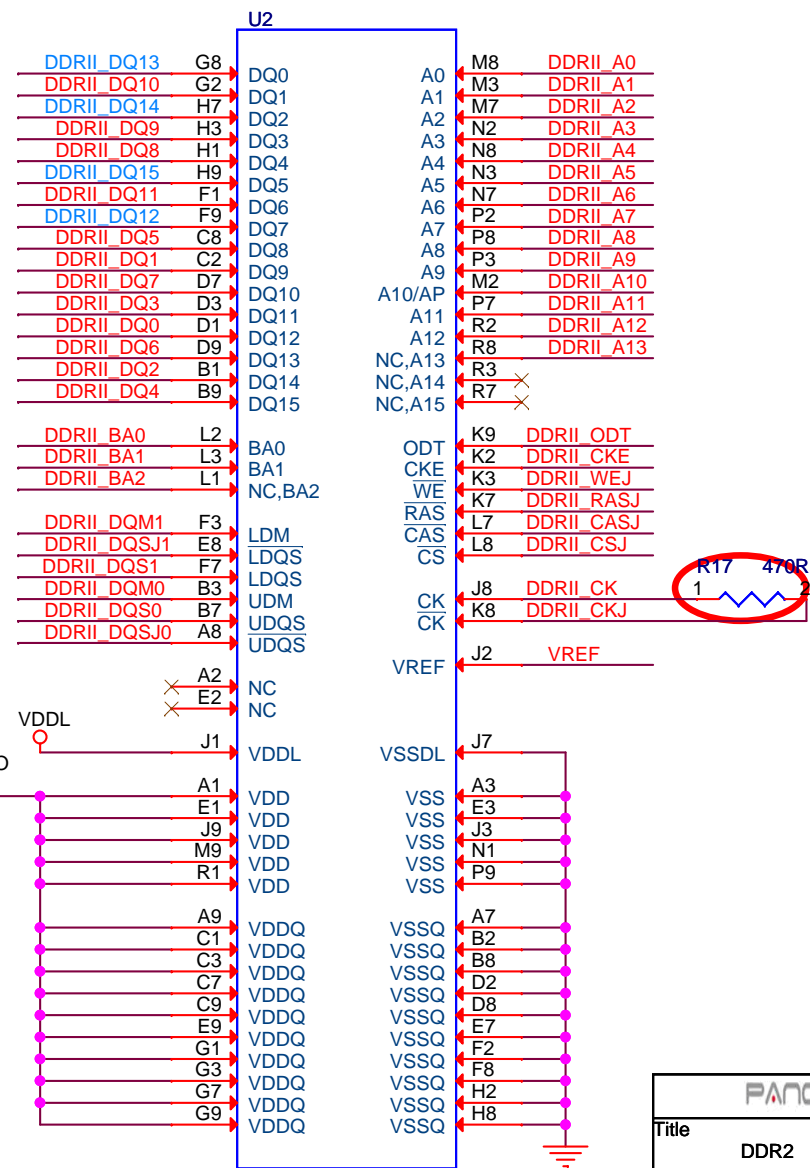
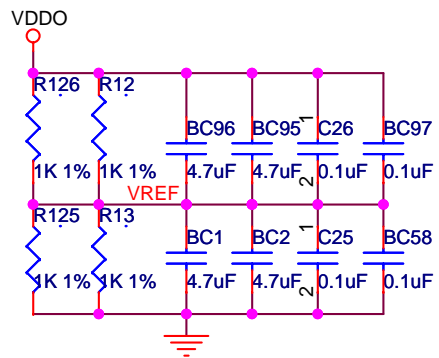
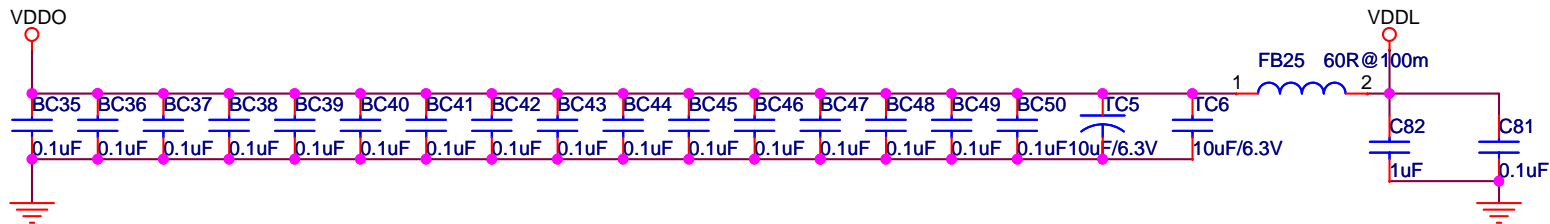


Verify Log:
 2012.9.12:更改PN8106电源参数: R134改为22R-0805
 R135改为8.2k-0805,D13改为1N4007



PANODIC 帕诺迪电器(深圳)有限公司		
Title BLOCK		
Size B	Document Number MC6391 (M3601E-MN88472+MXL603)	Rev 1.0
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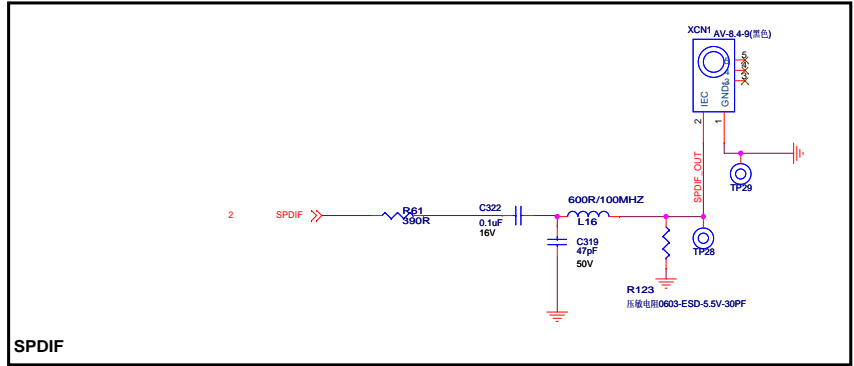
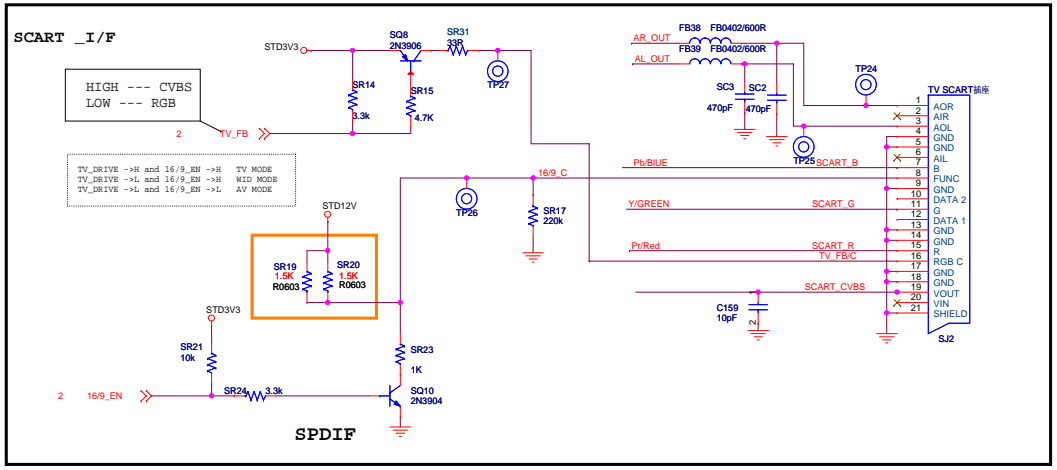
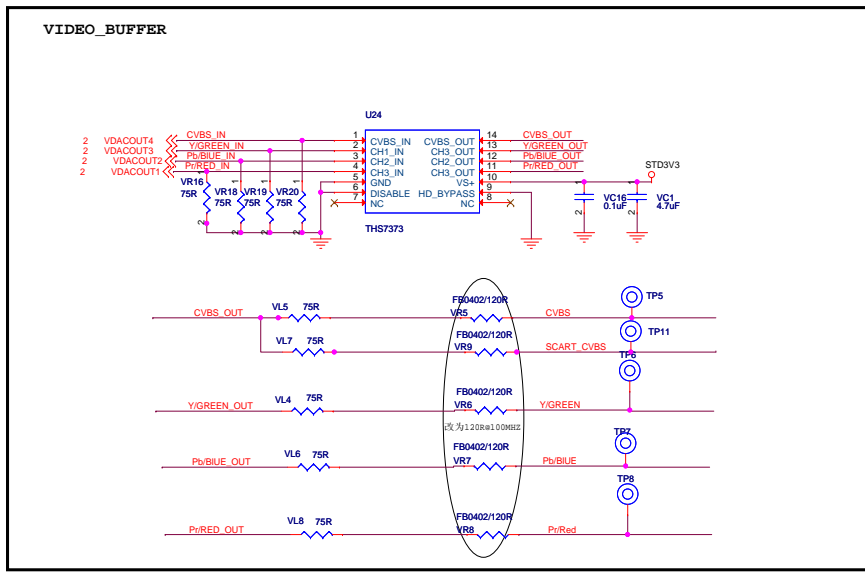
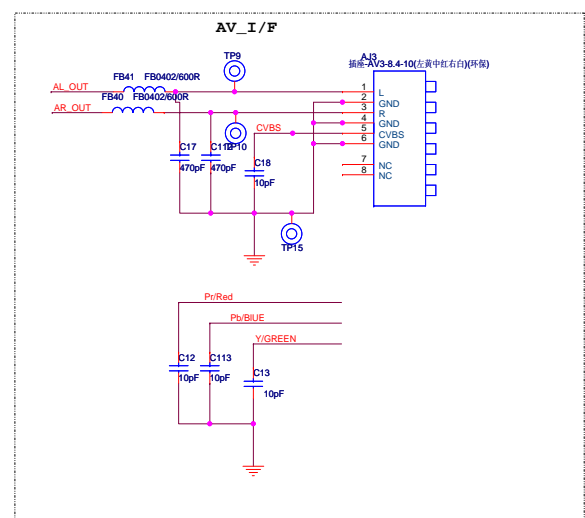
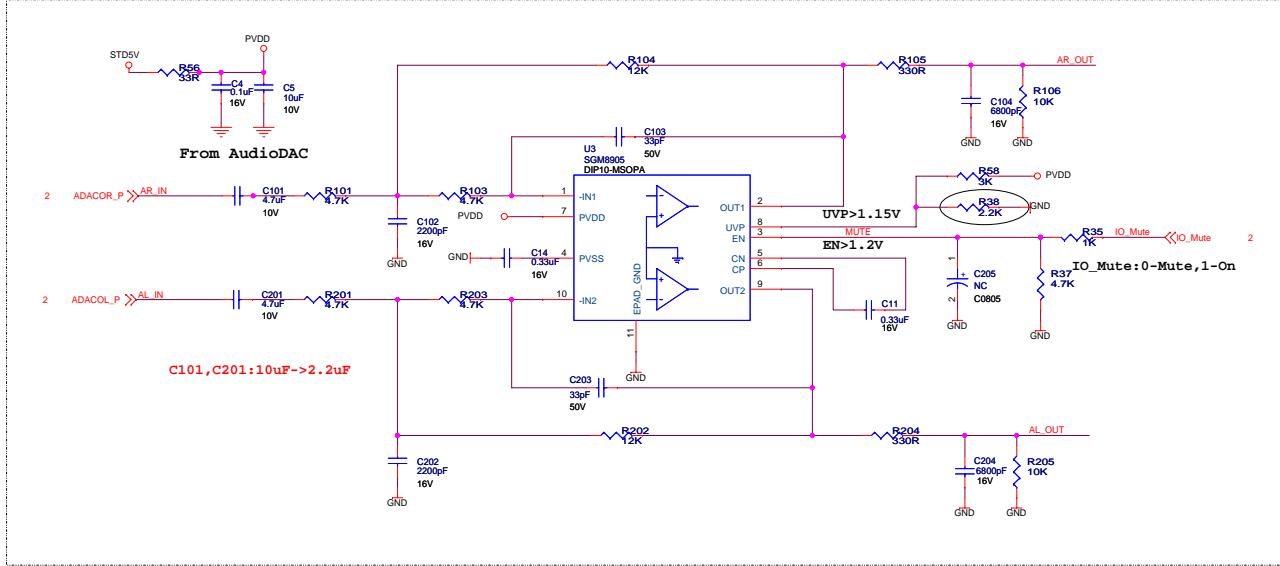


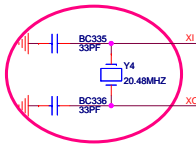
DDR-NT5TU64M16GG-AC- BGA84(环保)

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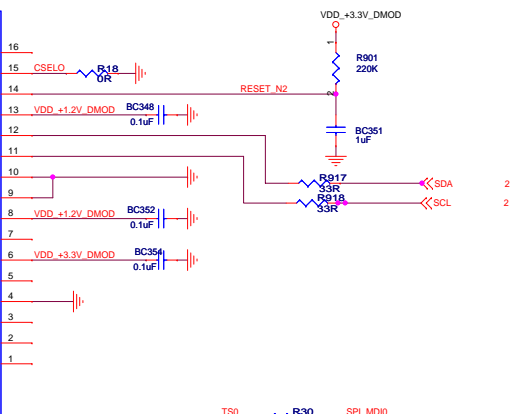
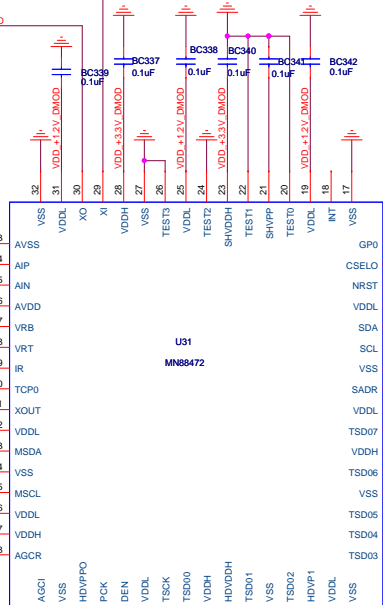
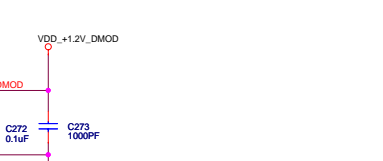
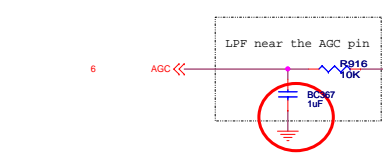
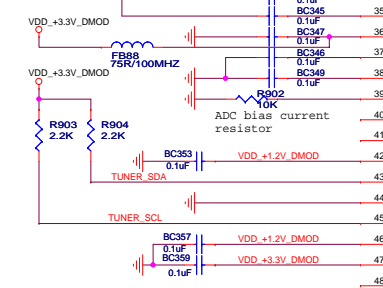
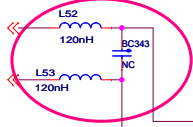
Title: **DDR2**

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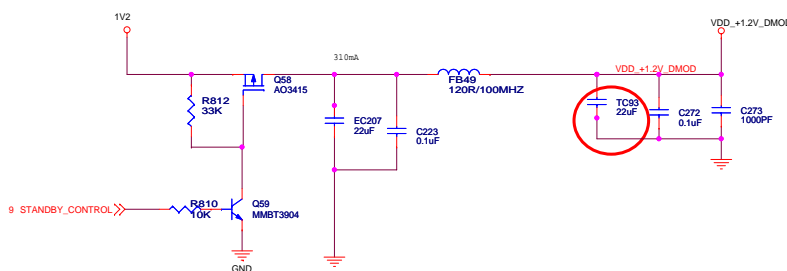
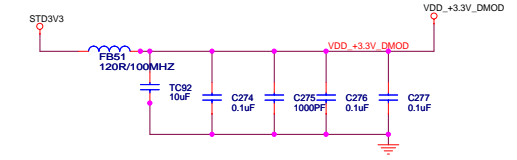


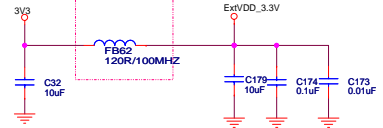
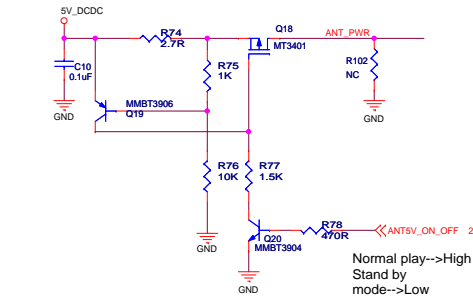
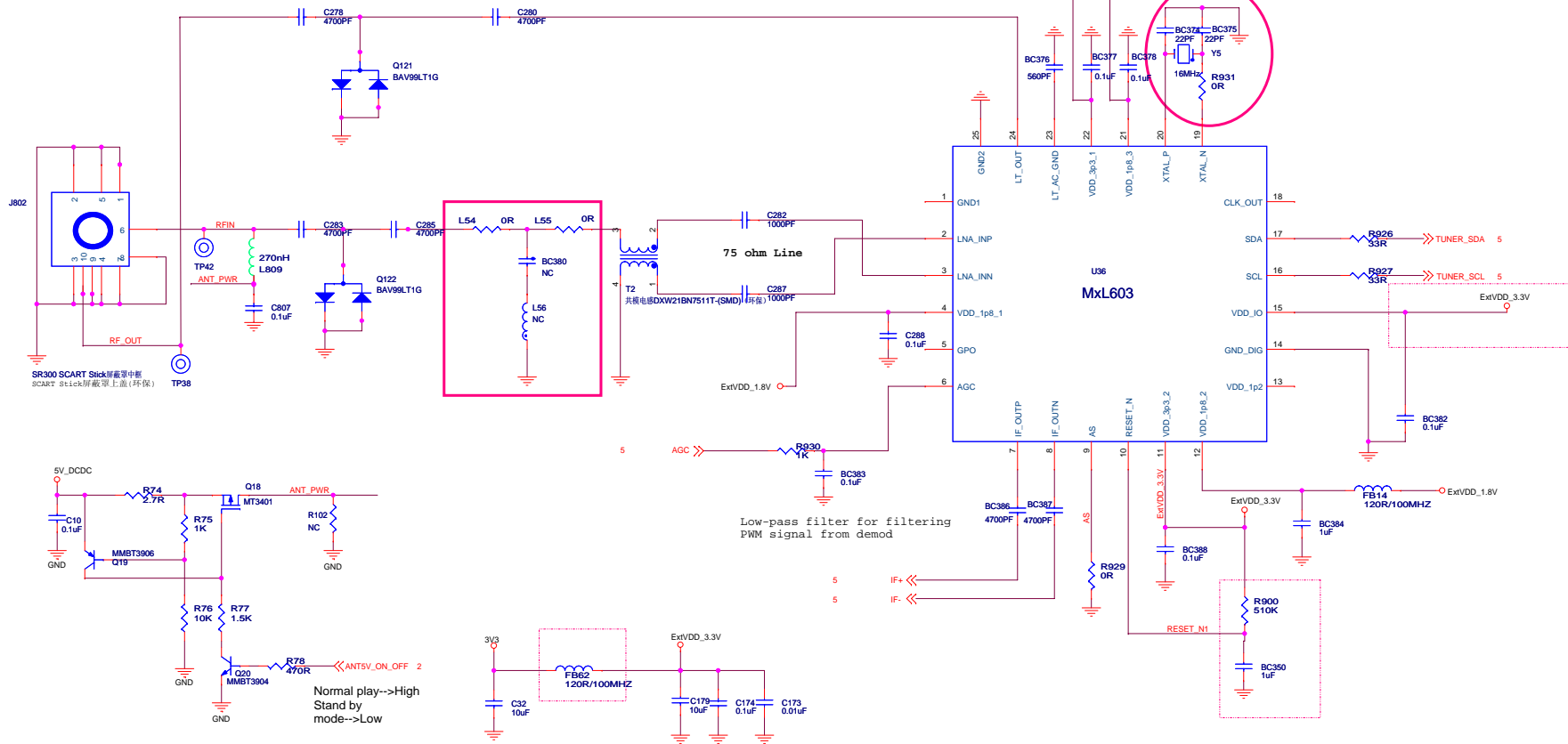


IF line: the shortest line, same length, line width >0.5mm
 IR pin resistance, tolerance is less than 1.0%, Ref: Panasonic: ERJ2RKF1002 (0402)



- SPI_MDI0 << SPI_MDI0 2
- SPI_CLK << SPI_CLK 2
- SPI_VALA << SPI_VALA 2
- SPI_SYNC << SPI_SYNC 2
- TUNER_SDA << TUNER_SDA 2
- TUNER_SCL << TUNER_SCL 2



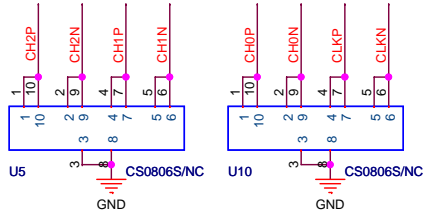
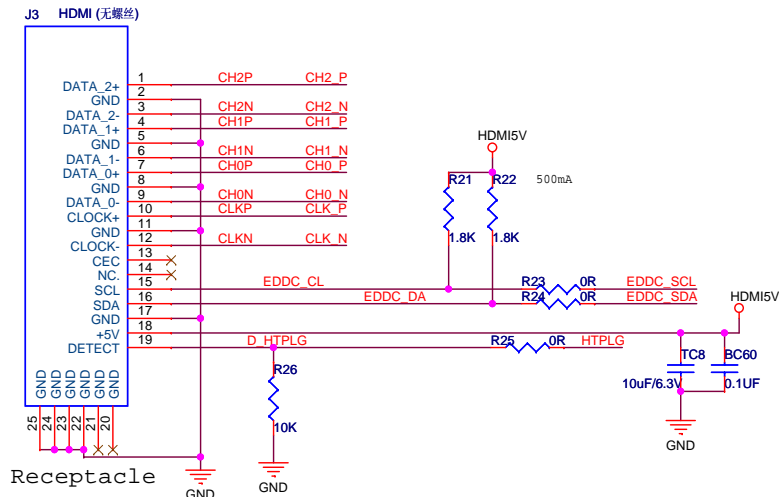
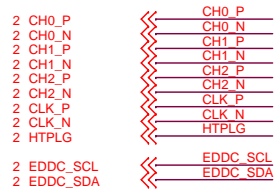


Low-pass filter for filtering PWM signal from demod

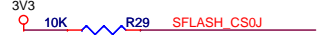
Normal play-->High
Stand by mode-->Low

PANCONC 帕诺迪电器(深圳)有限公司		
Title	MXL603	
Size	Document Number	Rev
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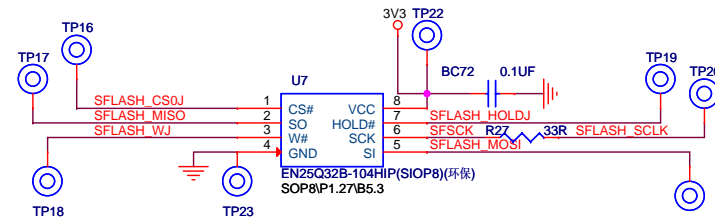
HDMI



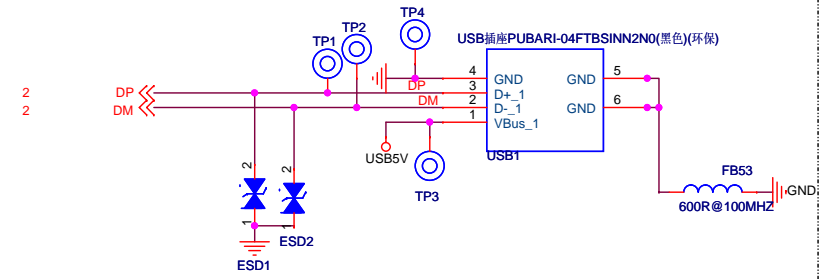
FLASH



FLASH I/F



USB



STRAP PIN



NOTE

Mem_CLK_SEL: STRAP6:5 (XRMII_TXD[1:0])
 00:396MHz (default)
 01:324MHz
 10:288MHz
 11:27MHz

CPU_CLK_SEL: STRAP7 (SFLASH_SCLK)
 0:396MHz (default)
 1:298MHz

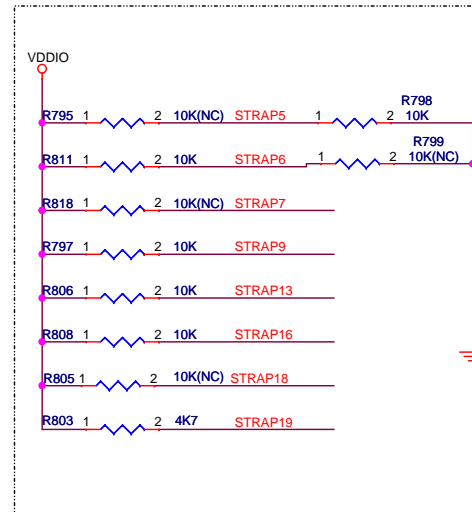
FLASH TYPE: STRAP9 (SFLASH_MOSI)
 0:parallel flash
 1:serial flash (default)

SPI_RMII_SEL: STRAP13 (SFLASH_HOLDJ)
 0: SPI left, RMII right (default)
 1: SPI right, RMII left

FUNCTION MODE: STRAP16 (UART_TX0)
 0: Test Mode
 1: Normal Working Mode (default)

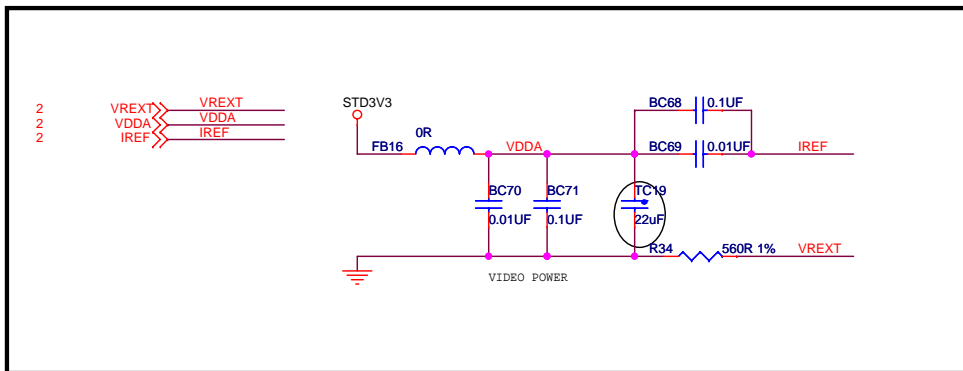
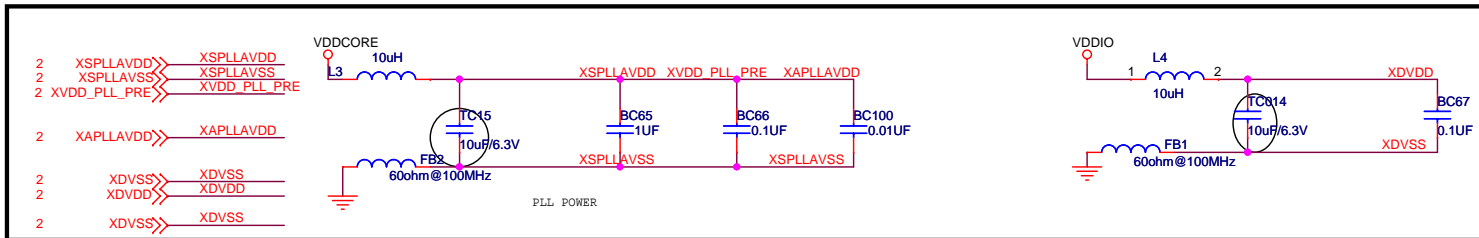
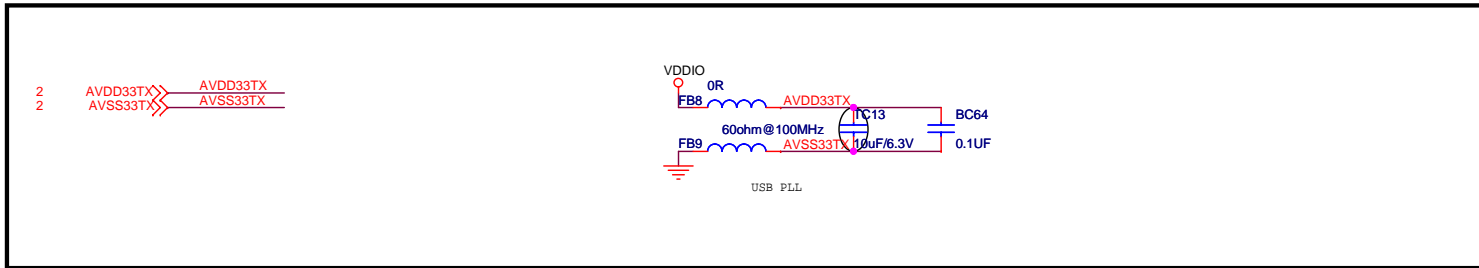
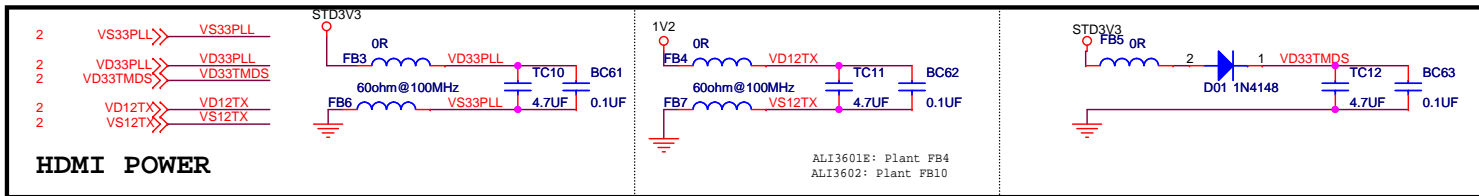
EROM (Single CPU SEL): STRAP18 (SFLASH_WJ)
 0: EROM Disable (default)
 1: EROM Enable
 (EJTAG selected):
 0: Dual CPU
 1: Single CPU

CPU_PROBE_ENABLE: STRAP19 (XRMII_TX_EN)
 0: Probe disable (default)
 1: Probe enable

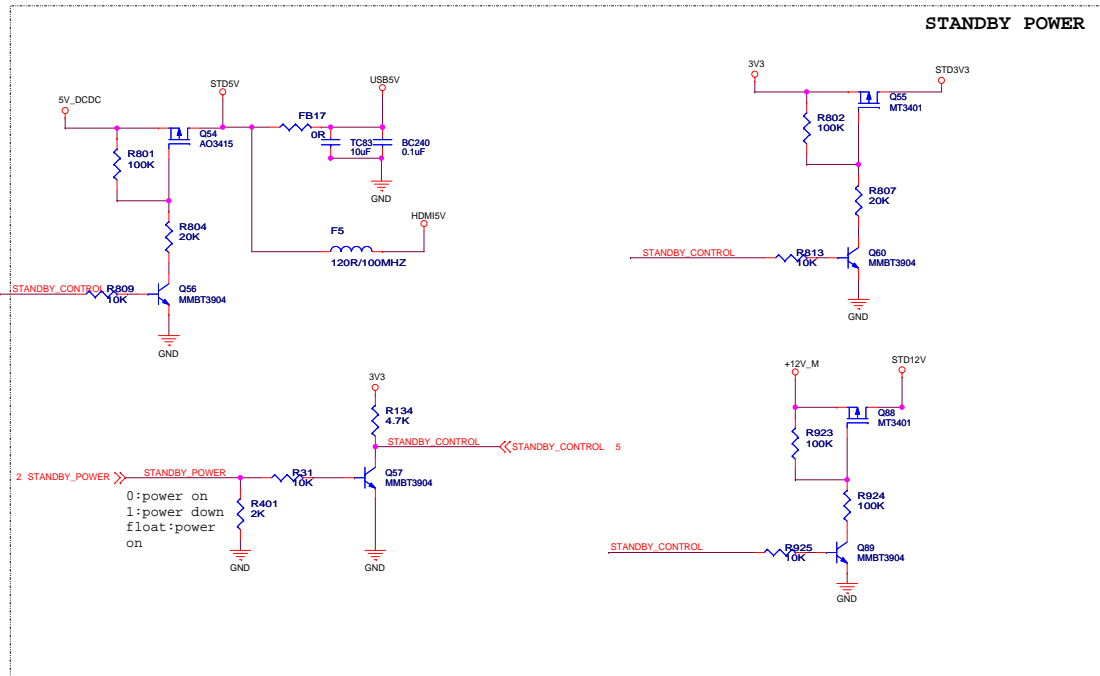
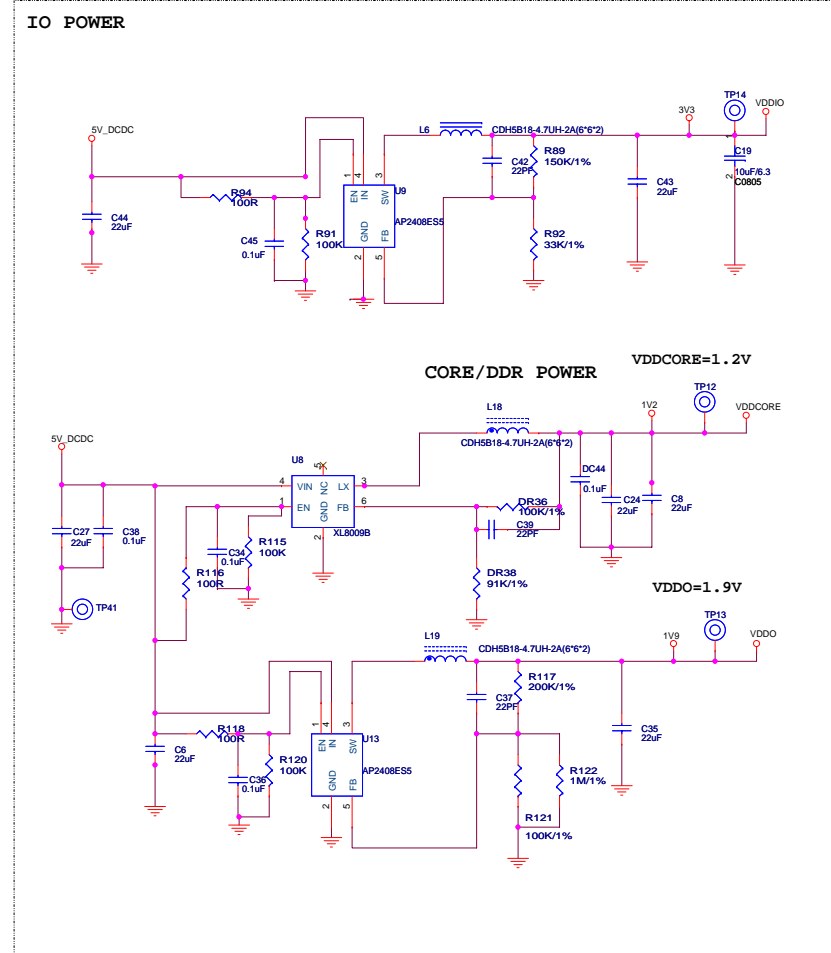
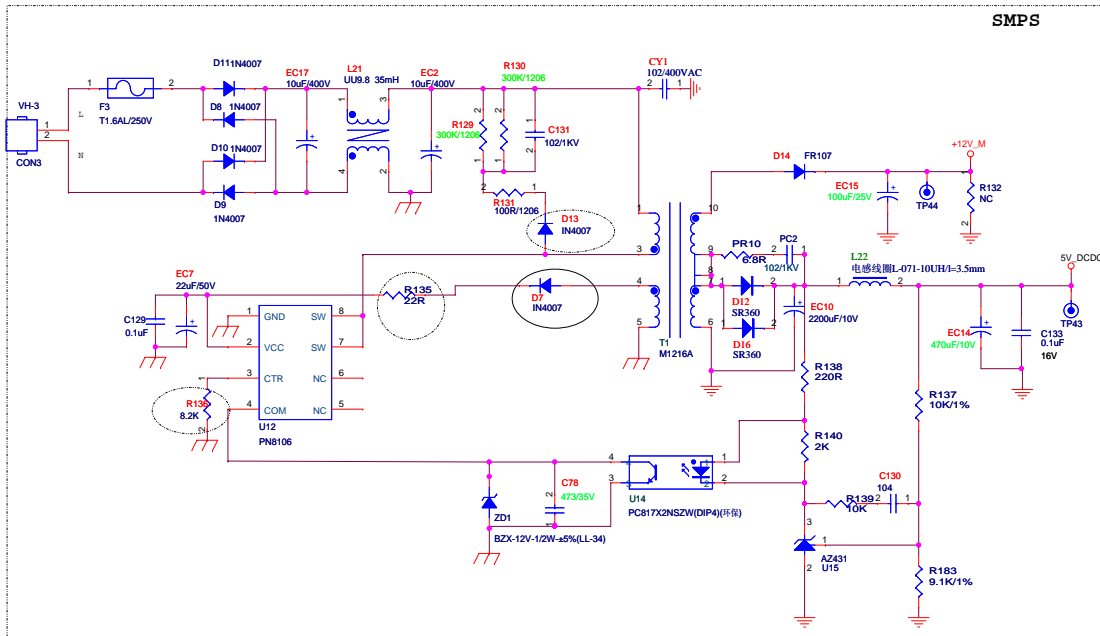


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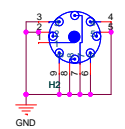
Title		
HDMI&USB%FLASH		
Size	Document Number	Rev
B	MC6382 (M3601E-MN88472+MXL603)	1.0
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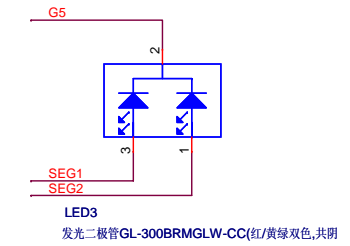
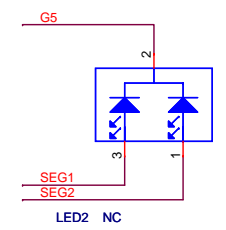
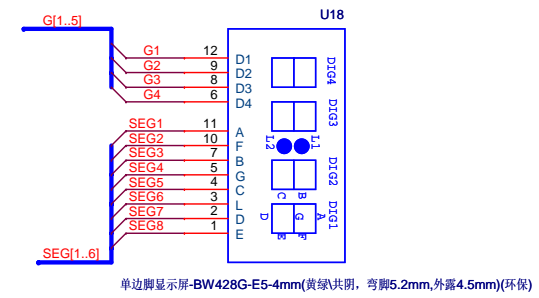
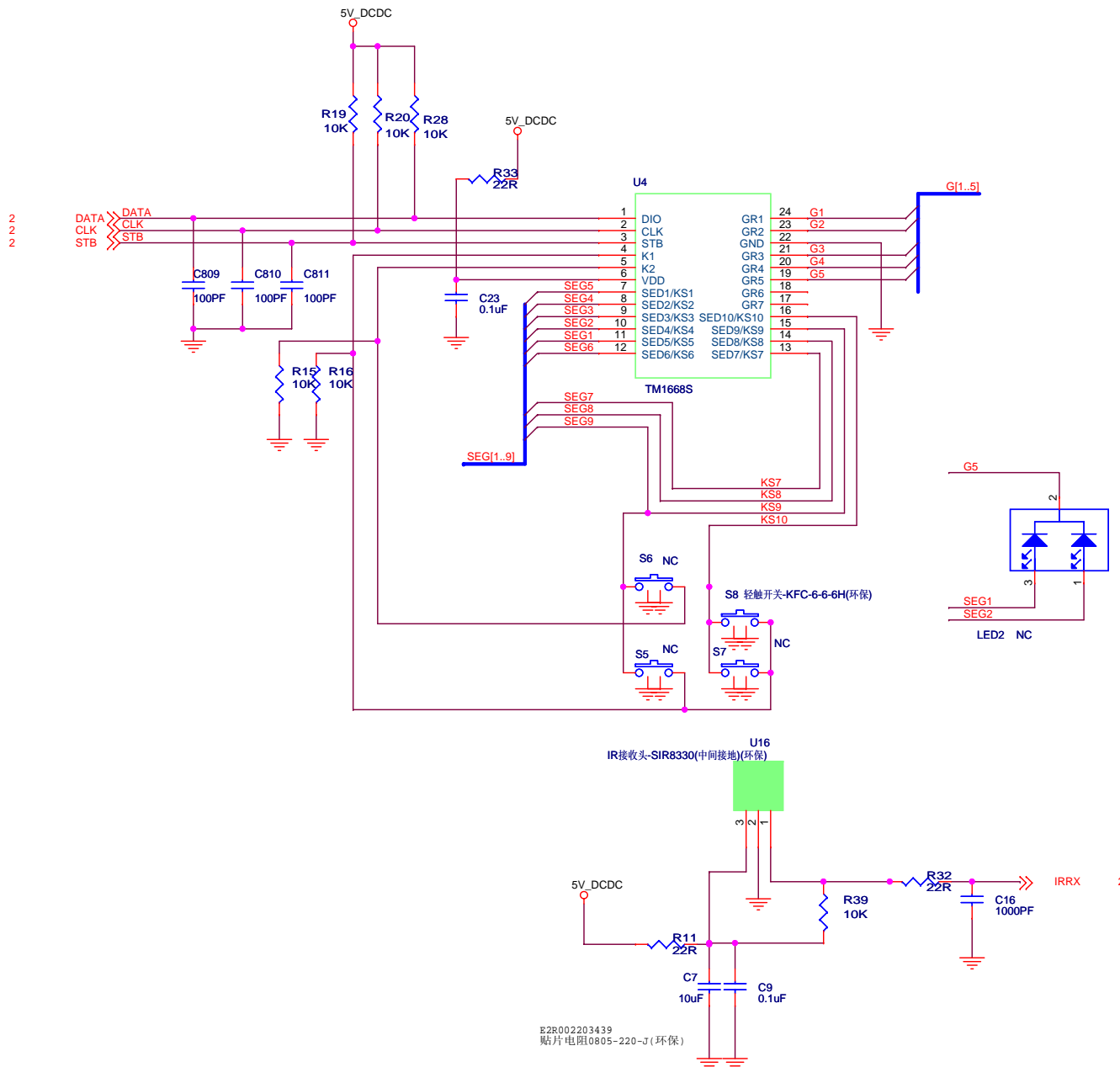
PANODIC 帕诺迪电器 (深圳) 有限公司		
Title	ANALOG	
Size B	Document Number MC6391 (M3601E-MN68472+MXL603)	Rev 1.0
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Mark



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Title DC-DC/SMP5			
Size C	Document Number MC6382 (M3601E-MN88472-MXL603)	Rev 1.0	
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Key Name	Function
S5	CH+
S6	CH-
S7 S8	Standby

注意:
 1、当此部分电路放在前面板上时, 请将贴片电阻及电容更换为0603封装。
 2、当个人设计电路中, 按键未达到最大化时, 请按照列表删除相关未用到的按键, 不要重新更改按键功能定义。

PANODIC 帕诺迪电器(深圳)有限公司		
Title: Front Panel		
Size B	Document Number: MC6391 (M3601E-MN88472+MXL603)	Rev: 1.0
Date: Monday, February 25, 2013	Sheet: 10	of 10